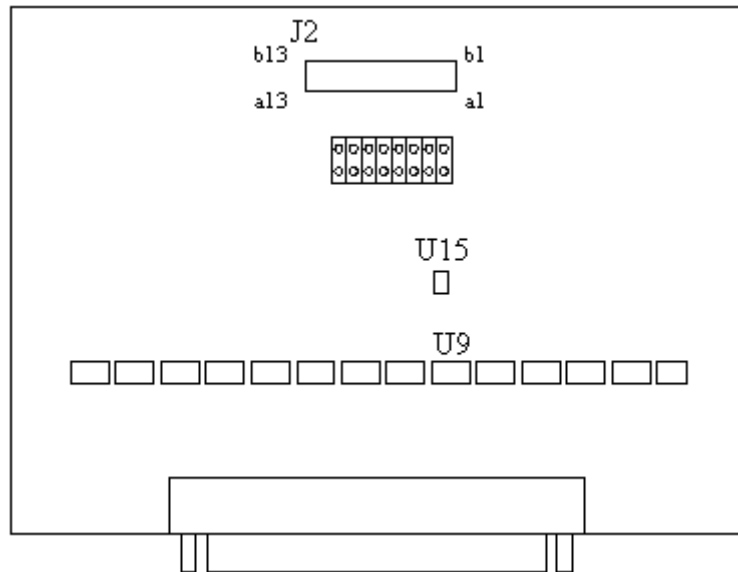


AD5430-71 (AUD interface board)

The drawing shows the arrangement of components on the substrate in the AUD relay box.



J3 pin arrangement

PIN No.	Signal name
1	AUD_RESET
2	AUD_MD
3	AUDATA3
4	AUDATA2
5	AUDATA1
6	AUDATA0
7	AUDCK
8	AUDSYNC#
9	Use prohibited
10	Use prohibited
11	Use prohibited
12	Use prohibited
13	+5V [SH Side]
14	GND [SH Side]

J2 pin arrangement

PIN No.	Signal name	PIN No.	Signal name
a1	AUDATA3	b1	GND [SH Side]
a2	AUDATA2	b2	GND [SH Side]
a3	AUDATA1	b3	GND [SH Side]
a4	AUDATA0	b4	GND [SH Side]
a5		b5	GND [SH Side]
a6	AUD_MD	b6	GND [SH Side]
a7	AUDCK	b7	GND [SH Side]
a8	AUD_RESET	b8	GND [SH Side]
a9	Reserved	b9	GND [SH Side]
a10	AUDSYNC#	b10	GND [SH Side]
a11	Fixed port	b11	GND [SH Side]
a12	Optional port	b12	GND [SH Side]
a13	+5V [SH Side]	b13	

Compatible connector
MIL standard (distance of contacts: 2.54 mm) 14 wicks

Compatible connector
ERNI